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Number of Pages (including this page)

Date: July 12, 2004

To: Examiner Remmon R. Forde – Group Art Unit 2826

Location: United States Patent and Trademark Office

Fax No.: 703.872.9306

From: Michael J. Balconi-Lamica 34,291

Subject: 10/677,573 John M. Grant

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**MESSAGE:**

Enclosed herewith, please find a Response to Restriction Requirement for filing in the below-identified application.

**Paid by Deposit Account: 503079, Freescale Semiconductor \$0.00**

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EXAMINER: Remmon R. Forde  
GROUP ART UNIT: 2826  
SERIAL NO.: 10/677,573  
FILED: OCTOBER 2, 2003  
INVENTOR: JOHN M. GRANT

JUL 12 2004

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DOCKET NO.: SC12480TP

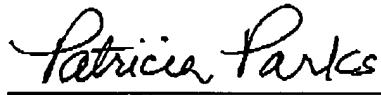
## UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S)	John M. Grant	GROUP ART UNIT:	2826
APPLN. NO.:	10/677,573	EXAMINER:	Remmon R. Forde
FILED:	October 2, 2003		
TITLE:	SEMICONDUCTOR DEVICE INCORPORATING A DEFECT CONTROLLED STRAINED CHANNEL STRUCTURE AND METHOD OF MAKING SAME		

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Patricia Parks

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This paper is in response to the restriction requirement in the Office Action mailed June 15, 2004.

REMARKS

In response to the restriction requirement, Applicant provisionally elects Group II (claims 11-20), without traverse, if applicable.

Reconsideration of this application is respectfully requested.

Respectfully submitted,



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